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PCT/EP00/05198 Docket No. Serial No. 112740-386 37 CFR 1.501 INFORMATION DISCLOSURE STATEMENT Applicant: IN A PATENT Charzinski. (use several sheets if necessary) Filing Date Group Art Unit **U.S. PATENT DOCUMENTS** Filing Date If Examiner's Initials Document Number Date Name Class Subclass appropriate AA AB AC AD ΑE ΑF AG AΗ ΑI ΑJ ΑK FOREIGN PATENT DOCUMENTS Translation Document Number Date Country Class Subclass Yes No 0418 130 A1 3-20-91 European AL WO 98/16037 4-16-98 PCT ΑM AN ΑO ΑP ΑQ AR AS R ART (Including Author, Title, Date, Pertinent Pages, Etc.) XP 000692431 Improvement in Bit Error Rate for Optoelectronic Multicomputer Interconnection Networks Using Cyclic Redundancy Code Error Detection, May et al XP 000286903 Performance Monitoring of Digital Networks, Woods et al. ΑU XP 000793869 Instantaneous In Service Fault Location Scheme for Digital Transmission Systems, Takasaki et al. Date Considered Examiner *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.